

GC2D008065E

Silicon Carbide Schottky Diode

V_{RRM}	= 650 V
$I_{F(Tc=150^\circ C)}$	= 8 A
Q_c	= 22 nC

Features

- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Positive Temperature Coefficient on VF

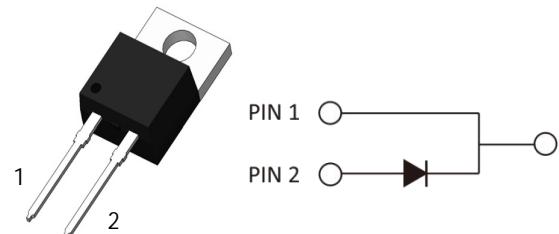
Benefits

- Replace Bipolar with Unipolar Rectifiers
- Higher System Efficiency
- Reduced Cooling Requirements
- Parallel Devices Without Thermal Runaway

Applications

- Switch Mode Power Supplies(SMPS)
- Server/Telecom Power Supplies
- Industrial Power Supplies
- Solar Inverters

Package



Part Number	Package
GC2D008065E	TO-220-2L

Maximum Ratings ($T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	650	V		
V_{RSM}	Surge Peak Reverse Voltage	650	V		
V_{DC}	DC Blocking Voltage	650	V		
I_F	Continuous Forward Current	8	A	$T_c=150^\circ C$	Fig. 7
I_{FRM}	Repetitive Peak Forward Surge Current	60	A	$T_c=25^\circ C$, $t_p=10$ ms, Half Sine Wave,	
I_{FSM}	Non-Repetitive Peak Forward Surge Current	75	A	$T_c=25^\circ C$, $t_p=10$ ms, Half Sine Wave	
$I_{F,Max}$	Non-Repetitive Peak Forward Surge Current	680	A	$T_c=25^\circ C$, $t_p= 10 \mu s$, Pulse	

P _{tot}	Power Dissipation	117 51	W	T _C =25°C T _C =110°C	Fig. 6
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C		

Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.45 1.75	1.70 2.00	V	I _F = 8 A T _J =25°C I _F = 8 A T _J =175°C	Fig. 1
I _R	Reverse Current	2 40	20 200	µA	V _R = 650 V T _J =25°C V _R = 650 V T _J =175°C	Fig. 2
Q _C	Total Capacitive Charge	22		nC	V _R = 400 V, T _J = 25°C Q _C = $\int_0^{V_R} C(V)dV$	Fig. 4
C	Total Capacitance	440 44 38		pF	V _R = 0 V, T _J = 25°C, f = 1 MHz V _R = 200 V, T _J = 25°C, f = 1 MHz V _R = 400 V, T _J = 25°C, f = 1 MHz	Fig. 3
E _c	Capacitance Stored Energy	5.8		µJ	V _R = 400 V	Fig. 5

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
R _{θJC}	Thermal Resistance from Junction to Case	1.28	°C/W	Fig. 8

Typical Performance

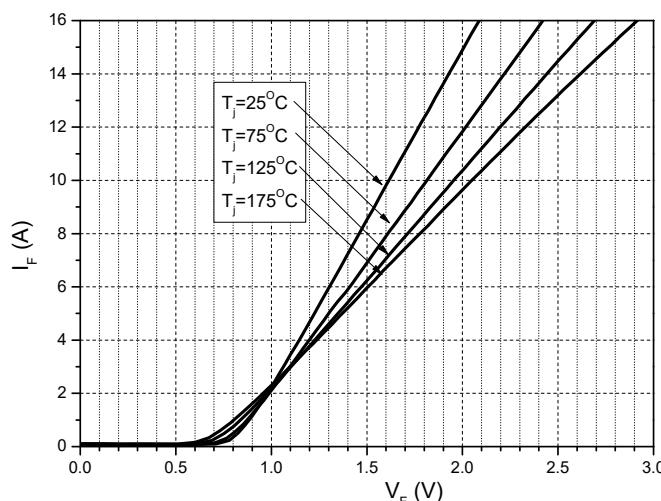


Figure 1. Forward Characteristics

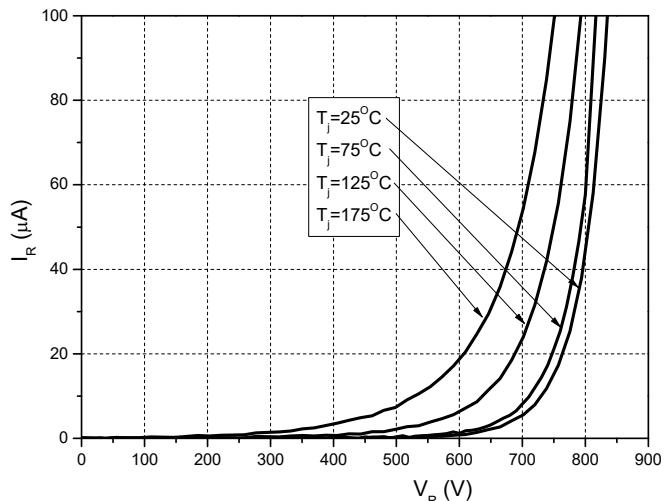


Figure 2. Reverse Characteristics

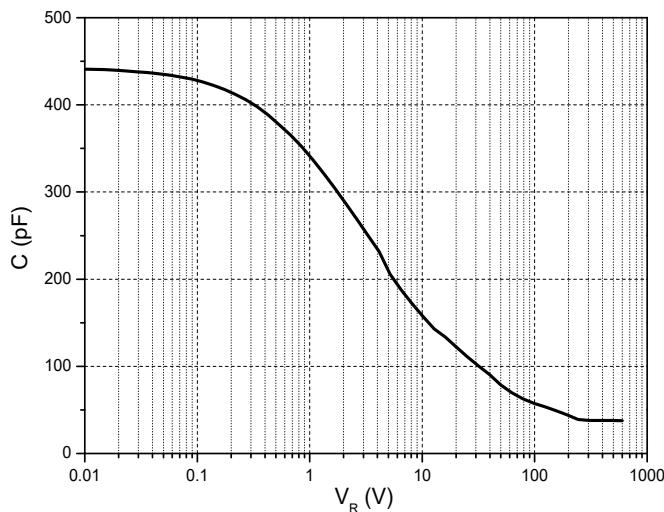


Figure 3. Capacitance vs. Reverse Voltage

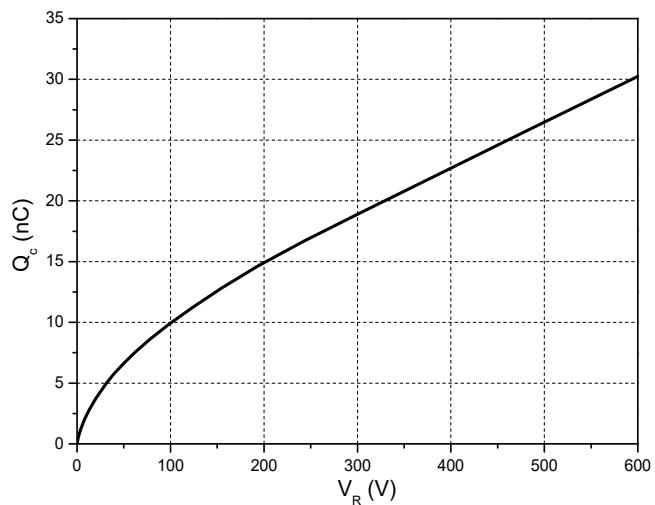


Figure 4. Total Capacitance Charge vs. Reverse Voltage

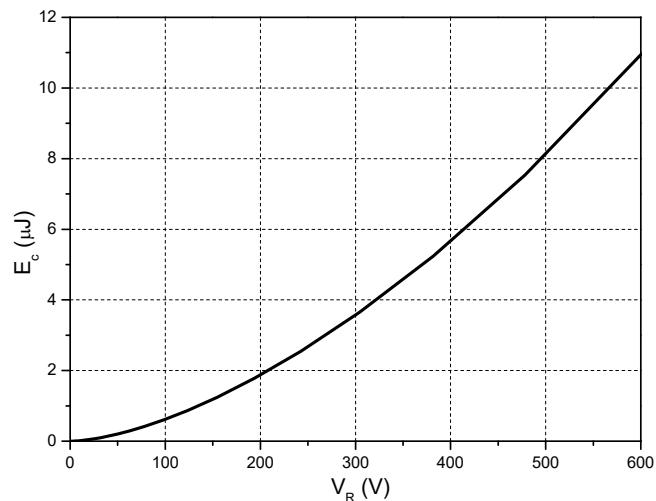


Figure 5. Capacitance Stored Energy

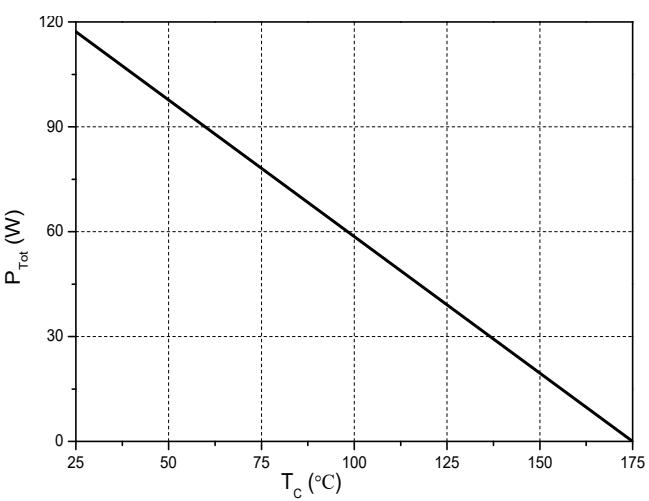


Figure 6. Power Derating

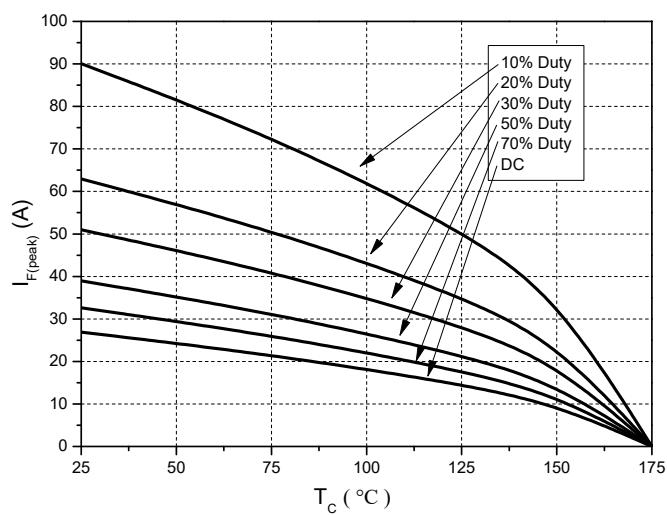


Figure 7. Current Derating

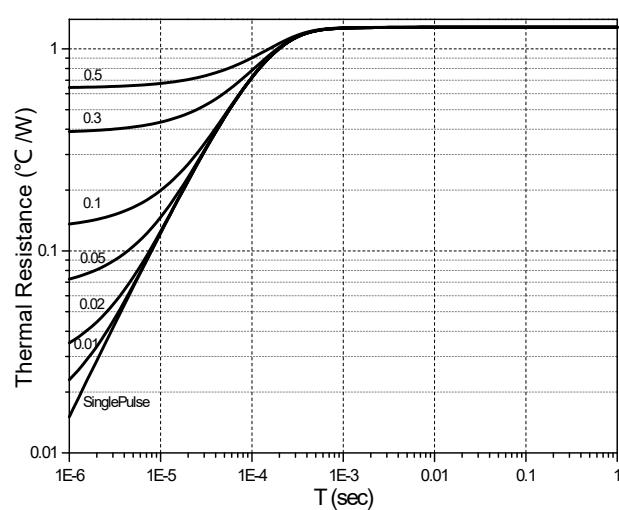
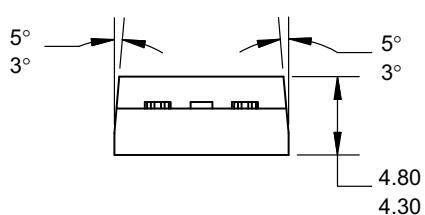
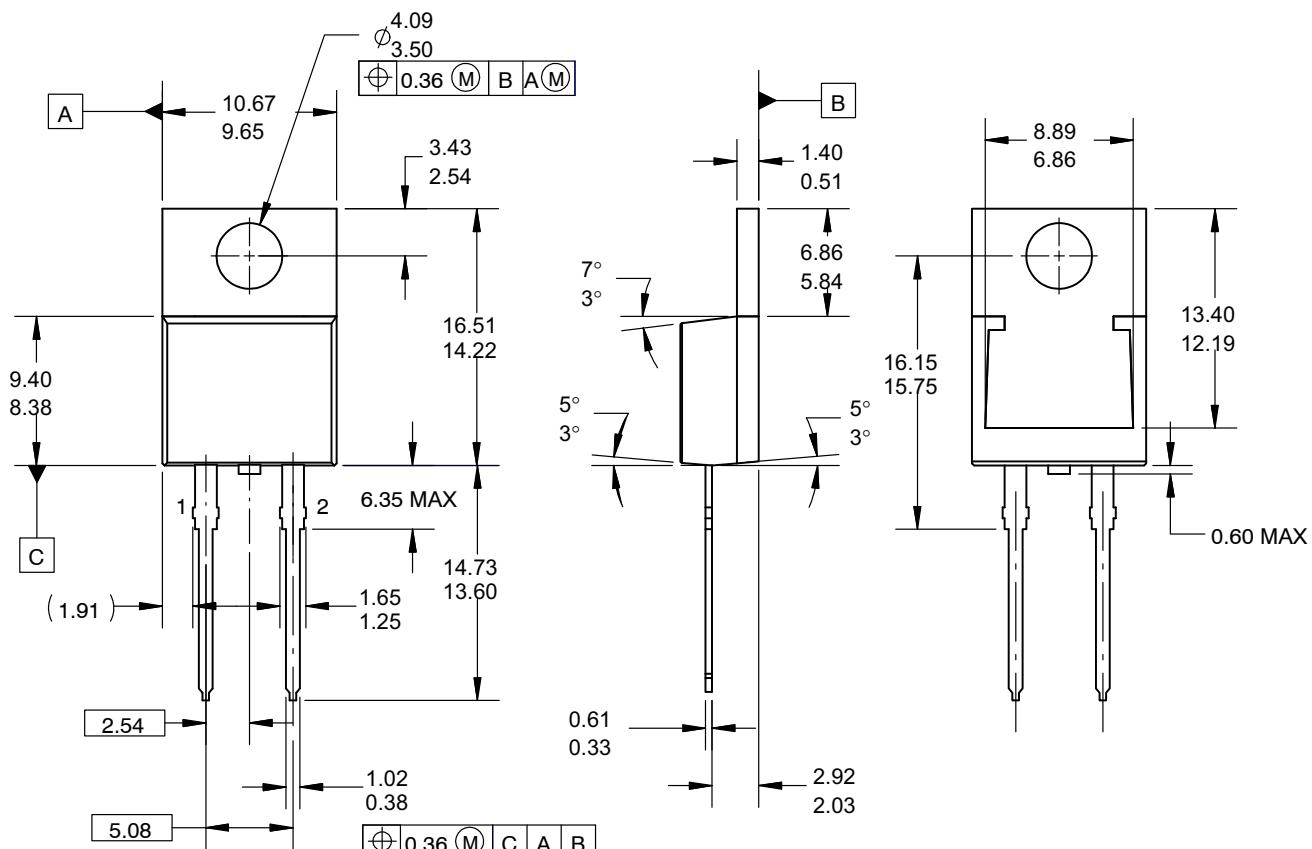


Figure 8. Transient Thermal Impedance

Package Dimensions: TO-220-2L



NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220, ISSUE K,
VARIATION AC, DATED APRIL 2002.
 - B. ALL DIMENSIONS ARE IN MILLIMETERS.
 - C. DIMENSION AND TOLERANCE AS PER ASME
Y14.5-2009.
 - D. DIMENSIONS ARE EXCLUSIVE OF BURRS,
MOLD FLASH AND TIE BAR PROTRUSIONS.